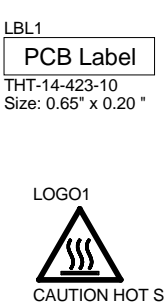
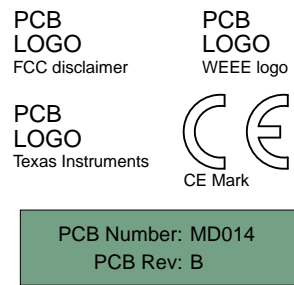


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|---------------------------------|------------------------------|------------------------------------|
| Orderable: DRV8874-Q1EVM | Designed for: Public Release | Mod. Date: 1/15/2020 |
| TID #: N/A | Project Title: DRV8874xEVM | |
| Number: MD014 | Rev: B | Sheet: 2 of 3 |
| SVN Rev: Not in version control | Assembly Variant: 002 | File: MD014B_SCH.SchDoc |
| Drawn By: | Size: B | Contact: http://www.ti.com/support |
| Engineer: Hector Hernandez | | |

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ZZ1 Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2 Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3 Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4 Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5 Assembly Note
ALL BOARDS MUST MEET THE UL MOT 130 C HEAT RATING

| Variant | Label Text |
|---------|---------------|
| 001 | DRV8876EVM |
| 002 | DRV8876-Q1EVM |
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